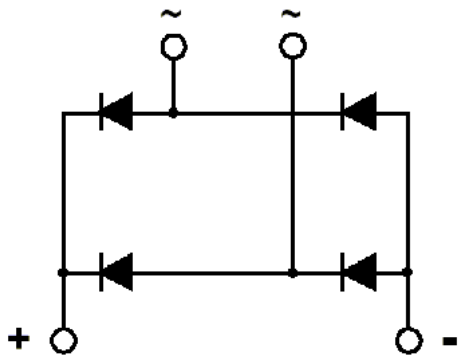
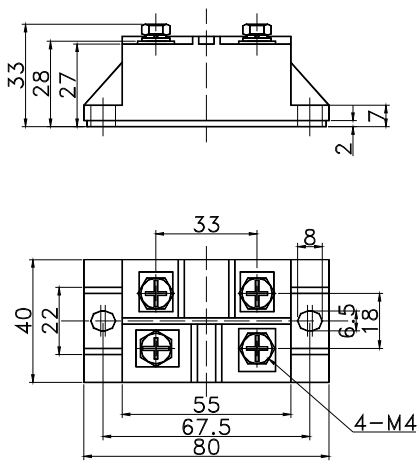
2D50

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T_j(°C)	VALUE			UNIT
				Min	Type	Max	
I_O	DC output current	Single-phase full wave rectifying circuit, T_C=100°C	150			50	A
V_{RRM}	Repetitive peak reverse voltage	V_{RRM} tp=10ms V_{RSM}= V_{DRM}&V_{RRM}+200V	150	600		1800	V
I_{RRM}	Repetitive peak current	at V_{RRM}	150			8	mA
I_{FSM}	Surge forward current	10ms half sine wave V_R=0.6V_{RRM}	150			0.75	KA
I^2t	I^2T for fusing coordination					2.8	A^2s*10^3
V_{FO}	Threshold voltage		150			0.80	V
r_F	Forward slop resistance					9.0	mΩ
V_{FM}	Peak forward voltage	I_{FM}=75A	25			1.547	V
$R_{th(j-c)}$	Thermal resistance Junction to heatsink	Single side cooled				0.55	°C /W
V_{iso}	Isolation voltage	50Hz,R.M.S,t=1min,I_{iso}:1mA(max)		2500			V
F_m	Terminal connection torque(M4)				1.5		N·m
	Mounting torque(M6)				3.0		N·m
T_{stg}	Stored temperature			-40		125	°C
W_t	Weight				200		g
Outline	207F4						

OUTLINE DRAWING & CIRCUIT DIAGRAM





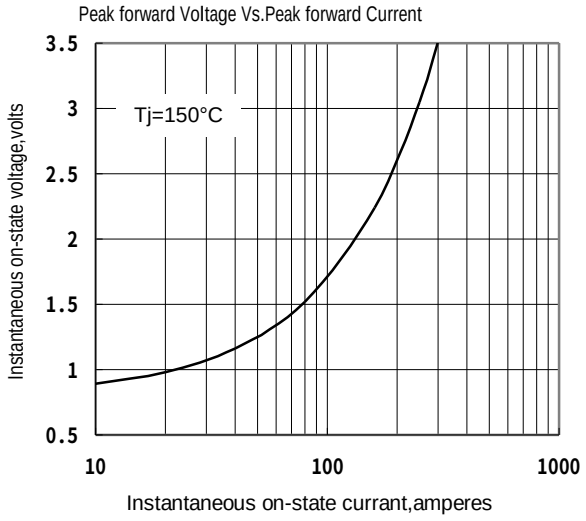


Fig.1

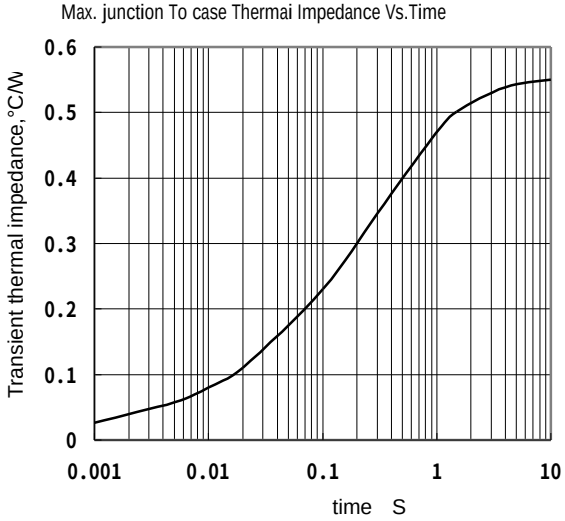


Fig.2

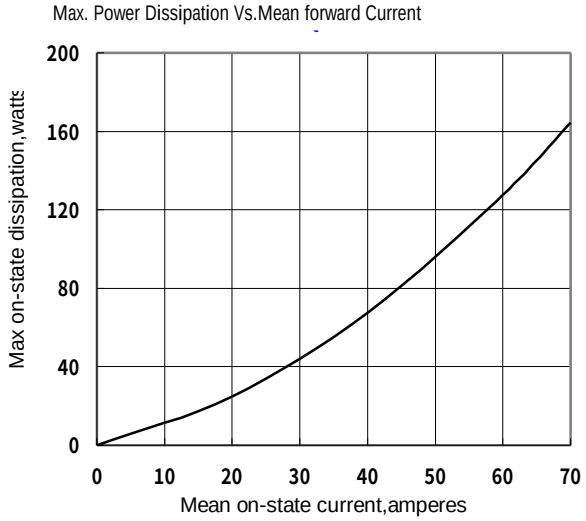


Fig.3

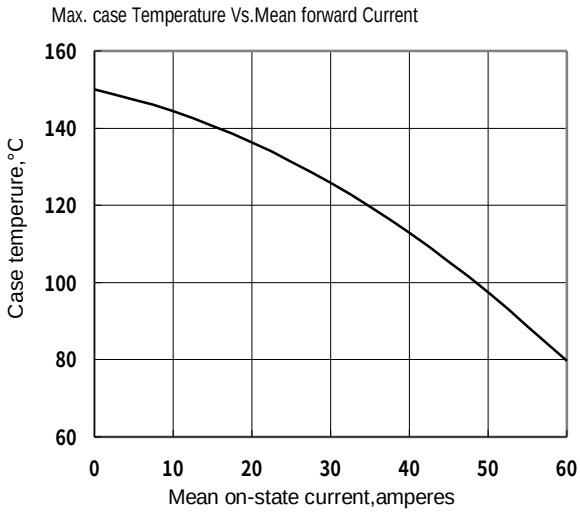


Fig.4

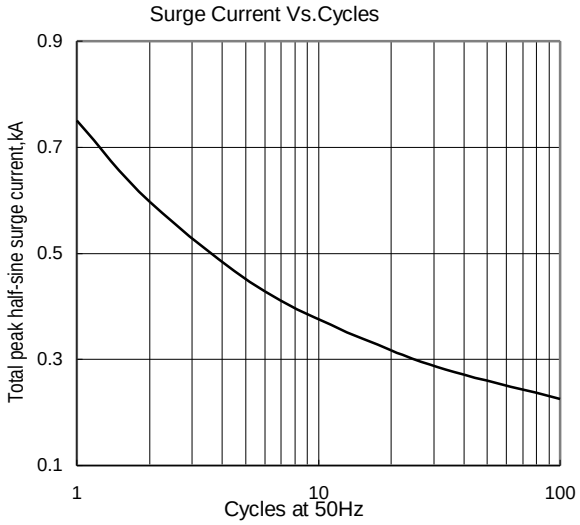


Fig.5

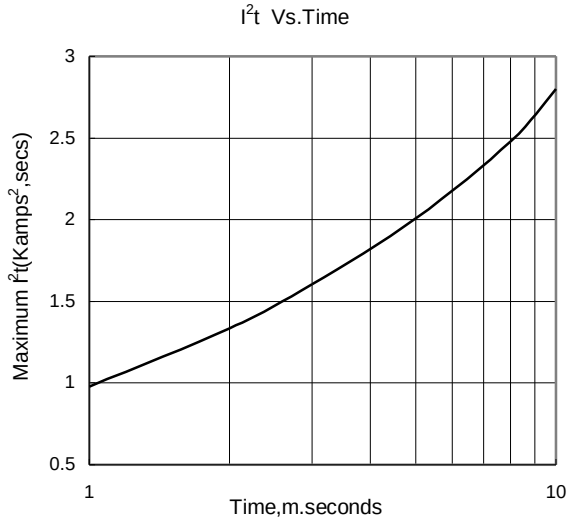


Fig.6