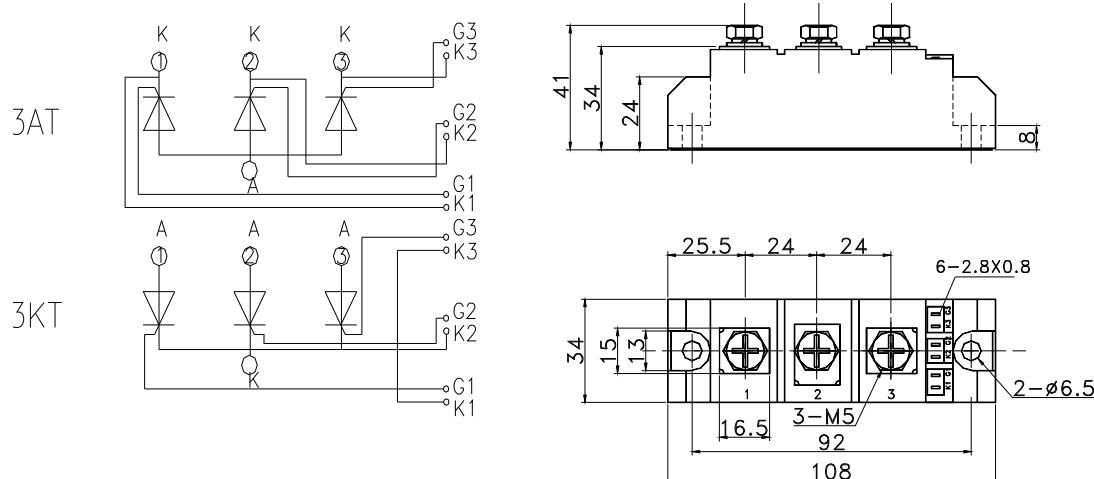


3AT100 3KT100 Charged part to case non-isolated

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	$T_J(^{\circ}\text{C})$	VALUE			UNIT
				Min	Type	Max	
$I_{T(\text{AV})}$	Mean on-state current	180° half sine wave 50Hz Single side cooled, $T_C=90^{\circ}\text{C}$	125			100	A
$I_{T(\text{RMS})}$	RMS on-state current	Single side cooled, $T_C=90^{\circ}\text{C}$	125			157	A
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	$V_{\text{DRM}} \& V_{\text{RRM}}$ tp=10ms $V_{\text{DsM}} \& V_{\text{RsM}} = V_{\text{DRM}} \& V_{\text{RRM}} + 200\text{V}$ respectively	125	800		1800	V
I_{DRM} I_{RRM}	Repetitive peak current	at V_{DRM} at V_{RRM}	125			12	mA
I_{TSM}	Surge on-state current	10ms half sine wave $V_R=60\%V_{\text{RRM}}$	125			3.20	KA
I^2t	I^2t for fusing coordination					52.2	$\text{A}^2\text{s} * 10^3$
V_{TO}	Threshold voltage		125			0.80	V
r_T	On-state slop resistance					2.45	$\text{m}\Omega$
V_{TM}	Peak on-state voltage	$I_{\text{TM}}=300\text{A}$	125			1.67	V
dv/dt	Critical rate of rise of off-state voltage	$V_{\text{DM}}=67\%V_{\text{DRM}}$	125			800	$\text{V}/\mu\text{s}$
di/dt	Critical rate of rise of on-state voltage current	From 67% V_{DRM} to 300A, Gate source 1.5A $t \leq 0.5\mu\text{s}$ Repetitive	125			100	$\text{A}/\mu\text{s}$
I_{GT}	Gate trigger current			30		100	mA
V_{GT}	Gate trigger voltage	$V_A=12\text{V}, I_A=1\text{A}$	25	0.8		2.0	V
I_H	Holding current			20		100	mA
V_{GD}	Non-trigger gate voltage	At 67% V_{DRM}	125			0.2	V
$R_{\text{th(j-c)}}$	Thermal resistance Junction to heatsink	Single side cooled				0.250	$^{\circ}\text{C}/\text{W}$
F_m	Thermal connection torque(M5)					0.20	N·m
	Mounting torque(M6)					0.30	N·m
T_{stg}	Stored temperature				-40	140	$^{\circ}\text{C}$
W_t	Weight					220	g
Outline		208F4					

OUTLINE DRAWING & CIRCUIT DIAGRAM



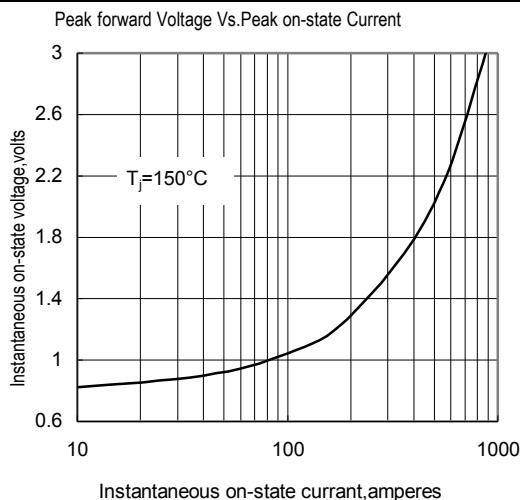


Fig.1

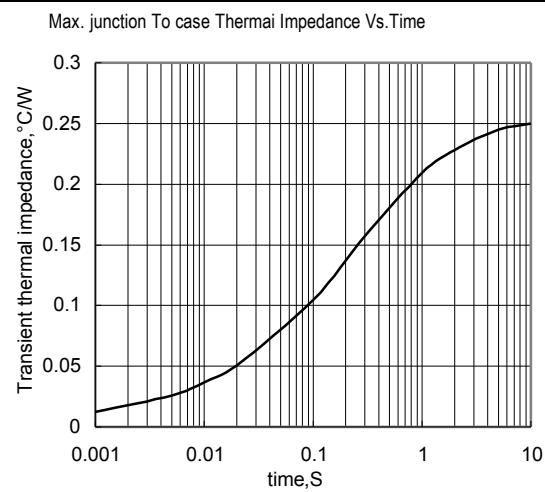


Fig.2

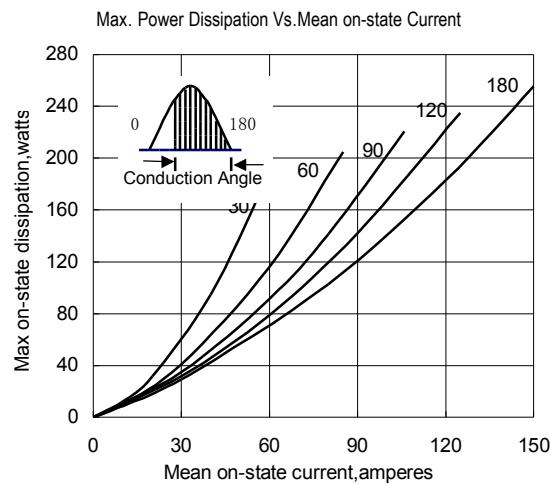


Fig.3

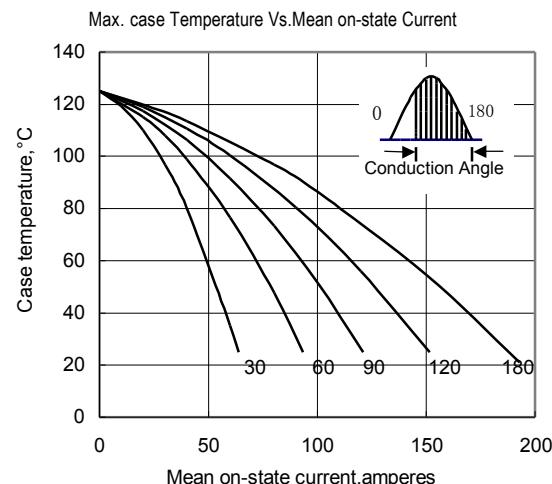


Fig.4

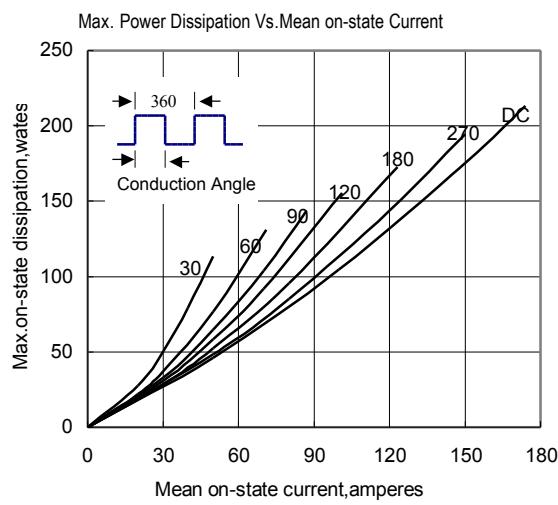


Fig.5

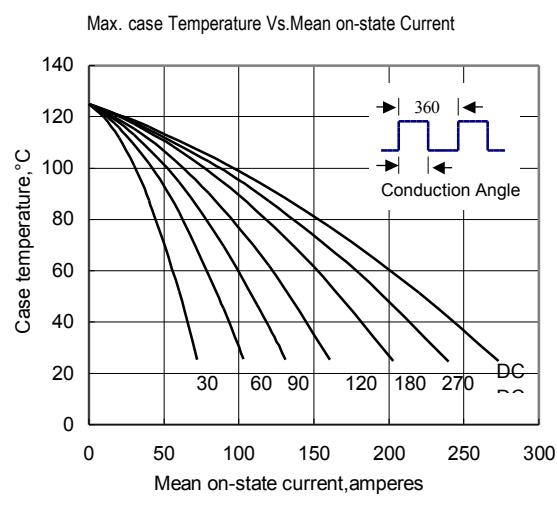


Fig.6