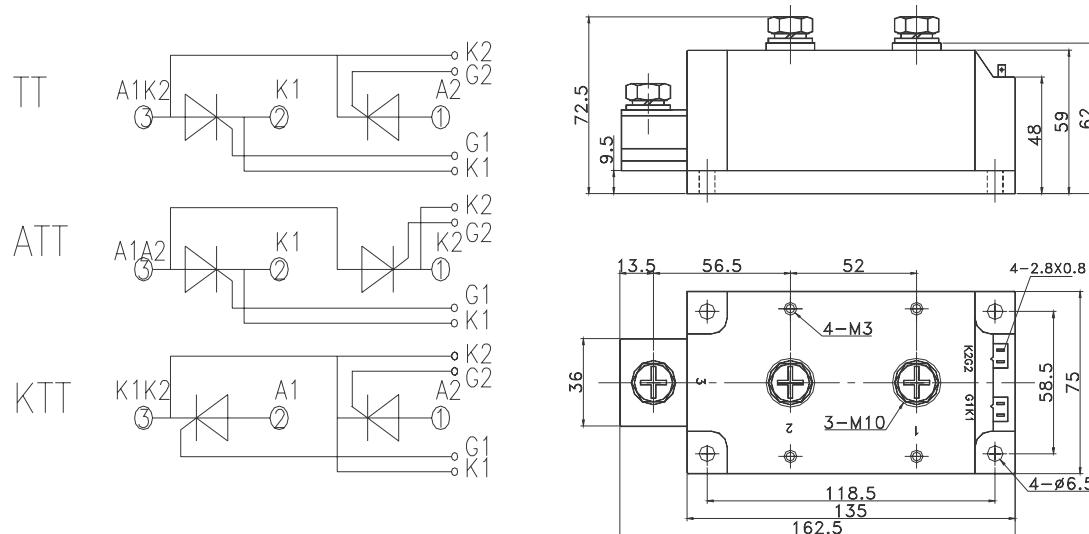


TT400 ATT400 KTT400

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T_j (°C)	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Single side cooled, $T_c=85^\circ C$	125			400	A
$I_{T(RMS)}$	RMS on-state current	as AC switch				840	A
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	$V_{DRM} \& V_{RRM}$ tp=10ms $V_{DsM} \& V_{RsM} = V_{DRM} \& V_{RRM} + 200V$ respectively	125	600		1800	V
I_{DRM} I_{RRM}	Repetitive peak current	at V_{DRM} at V_{RRM}	125			40	mA
I_{TSM}	Surge on-state current	10ms half sine wave	125			12.0	KA
I^2t	I^2T for fusing coordination	$V_R=60\%V_{RRM}$				734	$A^2s * 10^3$
V_{TO}	Threshold voltage	125				0.80	V
r_T	On-state slop resistance					0.49	m
V_{TM}	Peak on-state voltage	$I_{TM}=1200A$ 125				1.52	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=67\%V_{DRM}$ 125				800	V/s
di/dt	Critical rate of rise of on-state current	From 67% V_{DRM} to 1200A, Gate source 1.5A $t_r=0.5$ s Repetitive	125			100	A/s
I_{GT}	Gate trigger current	$V_A=12V$, $I_A=1A$ 25		30		200	mA
V_{GT}	Gate trigger voltage			1.0		3.0	V
I_H	Holding current			20		100	mA
V_{GD}	Non-trigger gate voltage	At 67% V_{DRM} 125				0.2	V
$R_{th(j-c)}$	Thermal resistance Junction to heatsink	At 180° sine Single side cooled				0.080	°C/W
V_{iso}	Isolation voltage	50Hz, R.M.S, t=1min, $I_{iso}:1mA$ (MAX)	2500				V
F_m	Thermal connection torque(M10)				0.60		Nm
	Mounting torque(M6)				0.30		Nm
T_{stg}	Stored temperature			-40		140	°C
W_t	Weight					2300	g
Outline			408F3				

OUTLINE DRAWING & CIRCUIT DIAGRAM



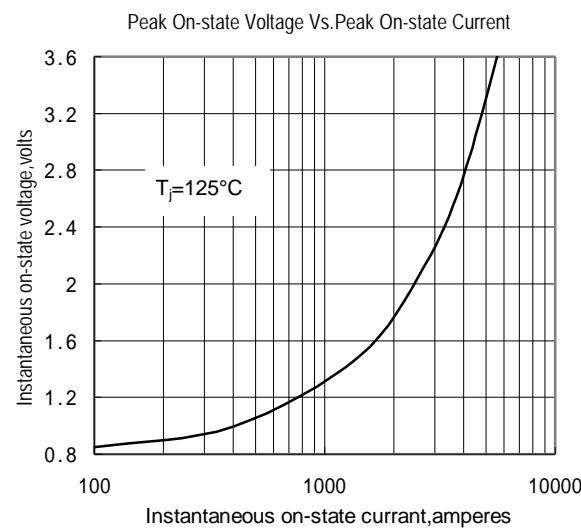


Fig.1

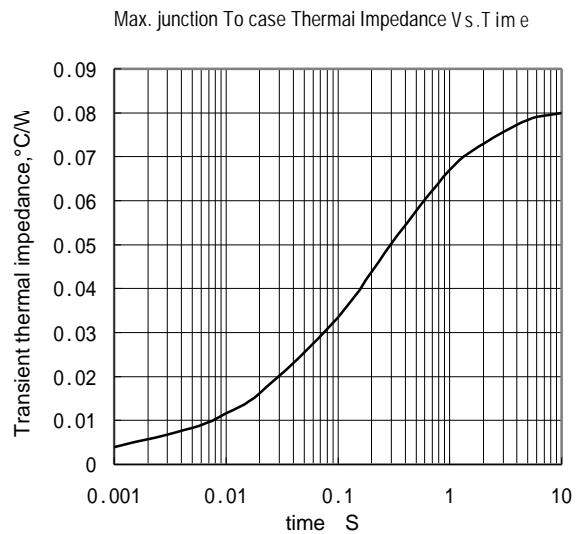


Fig.2

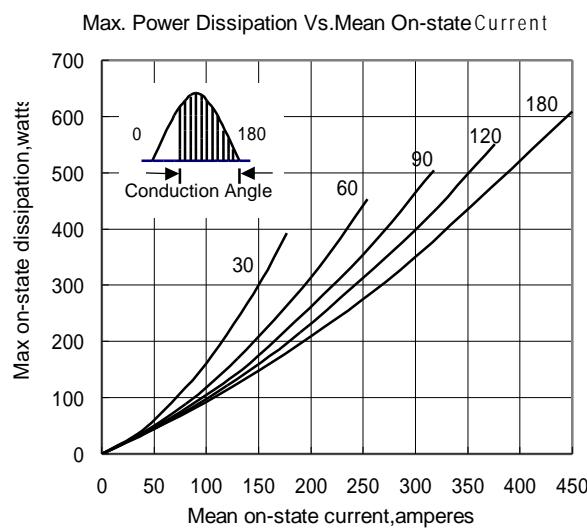


Fig.3

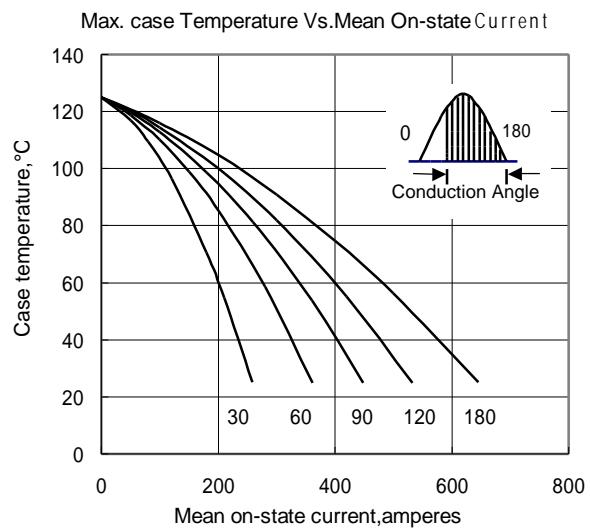


Fig.4

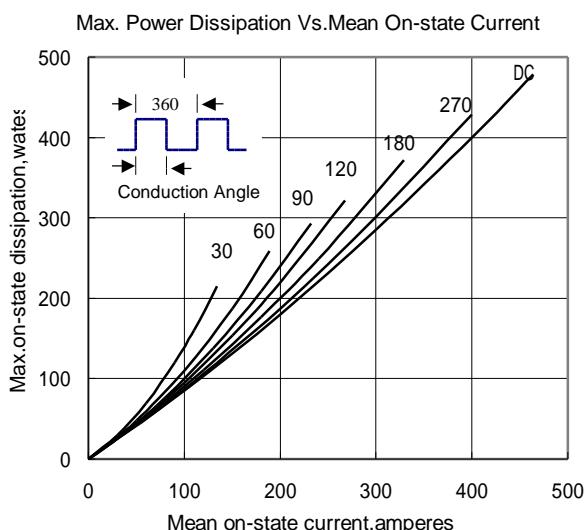


Fig.5

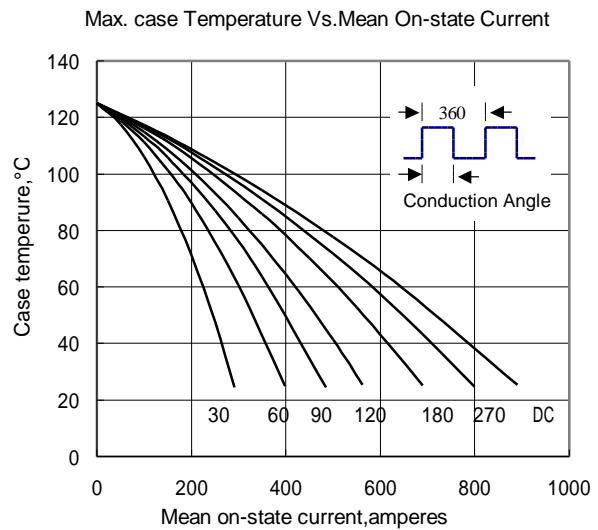


Fig.6

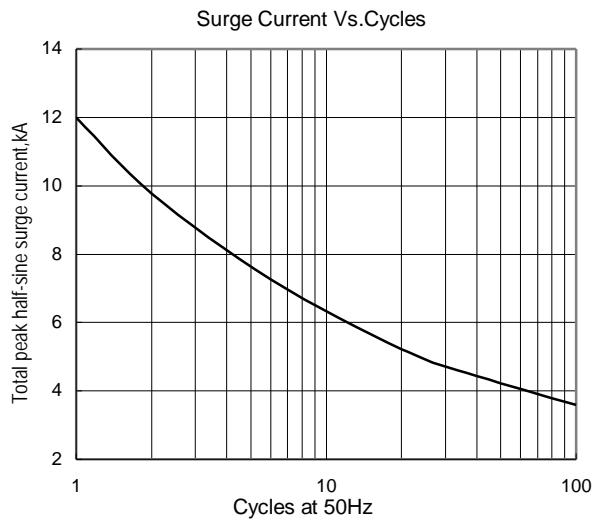


Fig.7

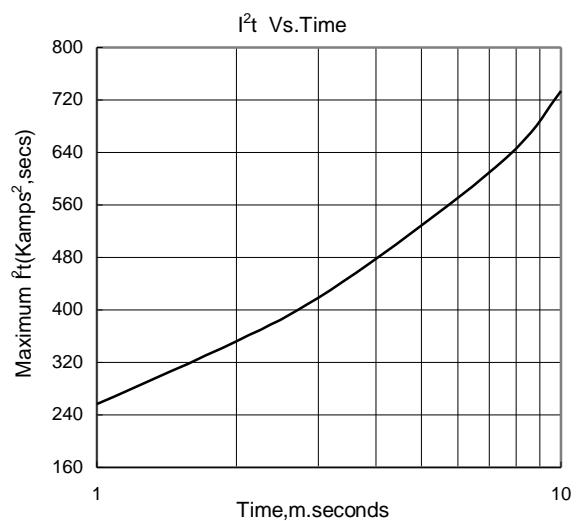


Fig.8

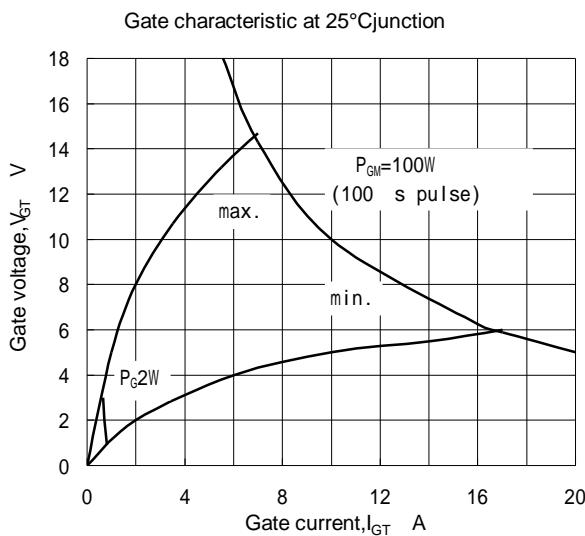


Fig.9

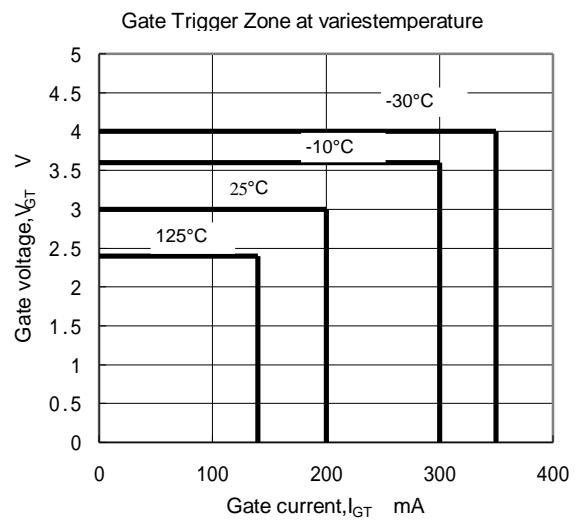


Fig.10