USR Semiconductor Co., Ltd

Bi08-XX00
Bi-DIRECTIONAL
THYRISTOR

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T_j(°C)	VALUE			UNIT
				Min	Type	Max	
$I_{T(RMS)}$	RMS current	50Hz sine wave Double side cooled,T_{hs}=55°C	125			1160	A
$I_{T(RMS)}$	RMS current	50Hz sine wave Double side cooled,T_{hs}=80°C	125			862	A
V_{RRM}	Repetitive peak reverse voltage	V_{RRM} tp=10ms V_{RsM}= V_{DRM}&V_{RRM}+100V	125	500		1800	V
I_{RRM}	Repetitive peak current	at V_{RRM}	125			50	mA
I_{TSM}	Surge on-state current	10ms half sine wave V_R=0.6V_{RRM}	125			6.8	KA
I^2T	I^2T for fusing coordination					231	A^2s*10^3
V_{TO}	Threshold voltage		125			1.0	V
r_T	On-state slop resistance					0.85	mΩ
V_{TM}	Peak on-state voltage	I_{TM}=1200A, F=18KN	125			2.02	V
dv/dt	Critical rate of rise of off-state voltage	V_{DM}=0.67V_{DRM}	125			50	V/μs
di/dt	Critical rate of rise of on-state current	From 67%V_{DRM} to 1500A, Gate source 1.5A t_r ≤0.5μs Repetitive	125			50	A/μs
I_{GT}	Gate trigger current	V_A=12V, I_A=1A	25	20		350	mA
V_{GT}	Gate trigger voltage			0.8		3.5	V
I_H	Holding current			20		400	mA
$R_{th(j-h)}$	Thermal resistance Junction to heatsink	At 180^0 sine' double side cooled Clamping force 18KN				0.035	°C /W
F_m	Mounting force			15		20	KN
T_{stg}	Stored temperature			-40		140	°C
W_t	Weight				360		g
Outline	KT39cT40						

Outline